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Integrated circuit - Wikipedia

4.2. 1 EOS/ESD SYMPOSIUM 95-175 A Comparison of Electrostatic Discharge Models and Failure Signatures for CMOS Integrated Circuit Devices M. Kelly

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Conclusion www.ti.com Plots in Figure 3 show the relationship of RÎ, JA versus the copper mounting pad area and its surface placement on the board. It is apparent that increasing copper mounting pad area considerably lowers RÎ, JA from approximately 110°C/W to 40°C/W in the range from 0.0123 to 1 square inches.

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Transistor - 101science.com

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Printed circuit board - Wikipedia

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TEST METHOD STANDARD MICROCIRCUITS - Quik-Pak

AEC - Q104 - REV- September 14, 2017 Page 4 of 25 Component Technical Committee Automotive Electronics Council 1.3.5 Multichip Module (MCM) Multiple active and/or passive sub-components interconnected to create a single complex circuit within

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