

error control coding fundamentals pdf

Turbo coding is an iterated soft-decoding scheme that combines two or more relatively simple convolutional codes and an interleaver to produce a block code that can perform to within a fraction of a decibel of the Shannon limit. Predating LDPC codes in terms of practical application, they now provide similar performance.. One of the earliest commercial applications of turbo coding was the ...

Error correction code - Wikipedia

A parity bit is a bit that is added to a group of source bits to ensure that the number of set bits (i.e., bits with value 1) in the outcome is even or odd. It is a very simple scheme that can be used to detect single or any other odd number (i.e., three, five, etc.) of errors in the output. An even number of flipped bits will make the parity bit appear correct even though the data is erroneous.

Error detection and correction - Wikipedia

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Defect is a deviation from the requirements specifications. For example there is Login feature, accordingly to requirement it shouldn't accept any special characters but developer has missed this the requirement and developed.

What is the difference between Error, defect, fault

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Bit Error Rate (BER) for BPSK modulation - dspLog

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Requirements are translated into design. Code is produced according to the design which is called development phase. After coding and development the testing verifies the deliverable of the implementation phase against requirements.

What are the Software Development Life Cycle (SDLC) phases?

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